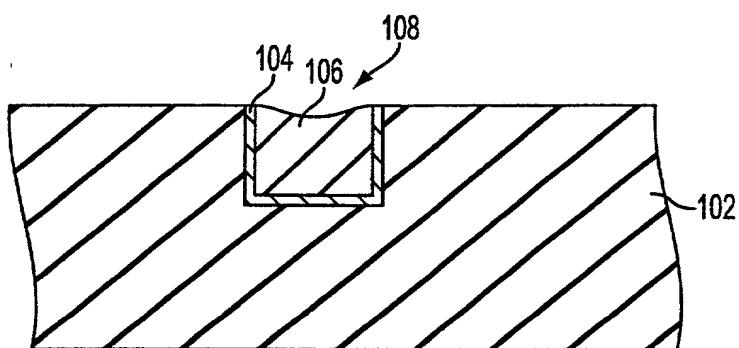
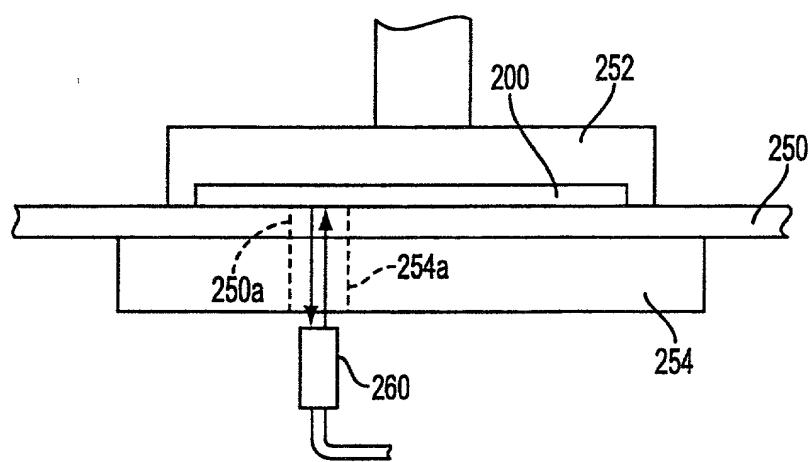
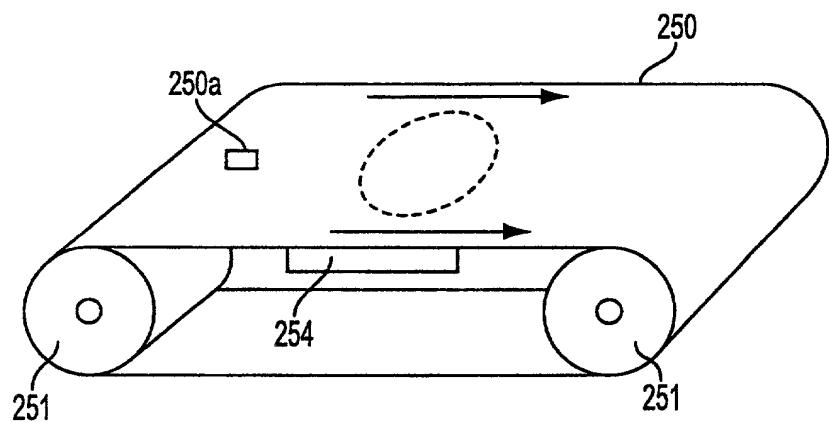


**FIG. 1A**  
(PRIOR ART)



**FIG. 1B**  
(PRIOR ART)



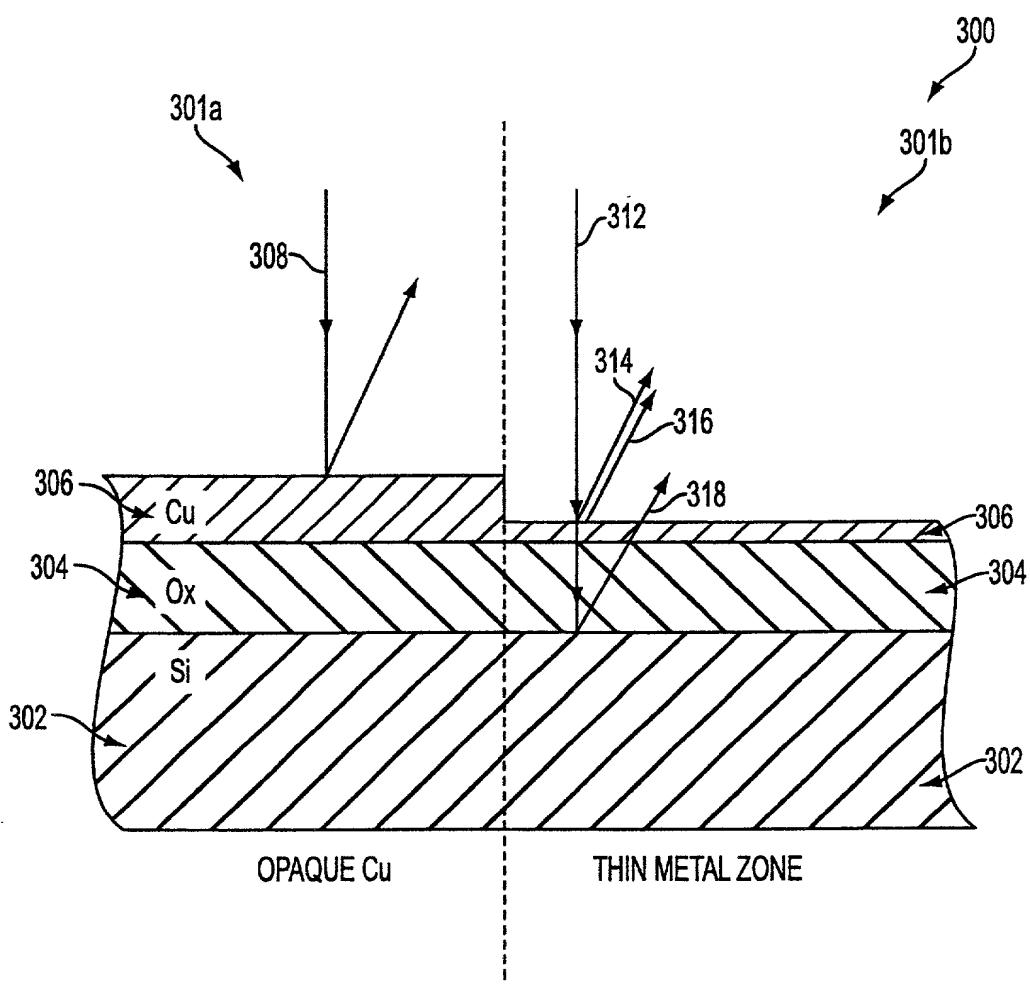
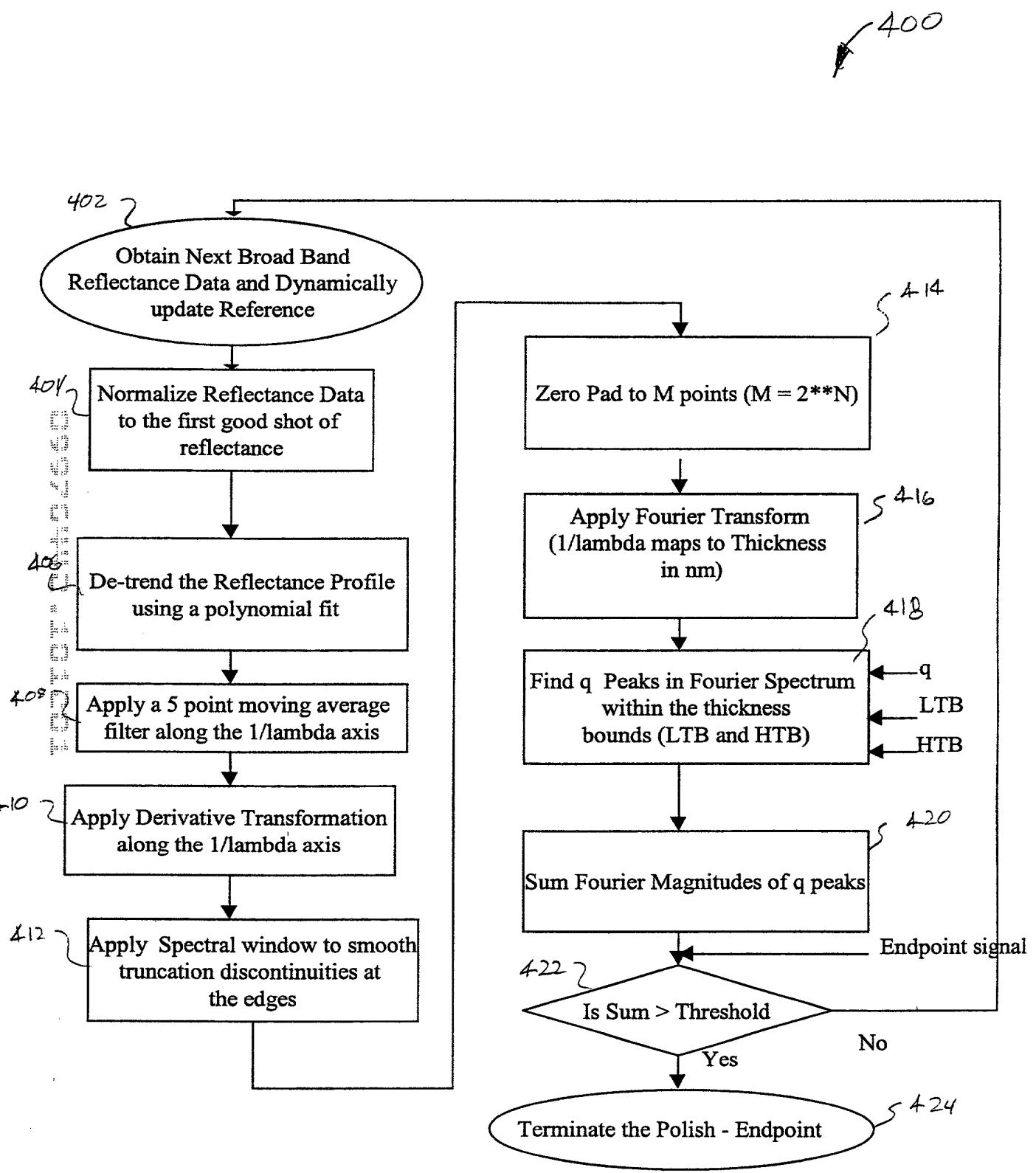


FIG. 3



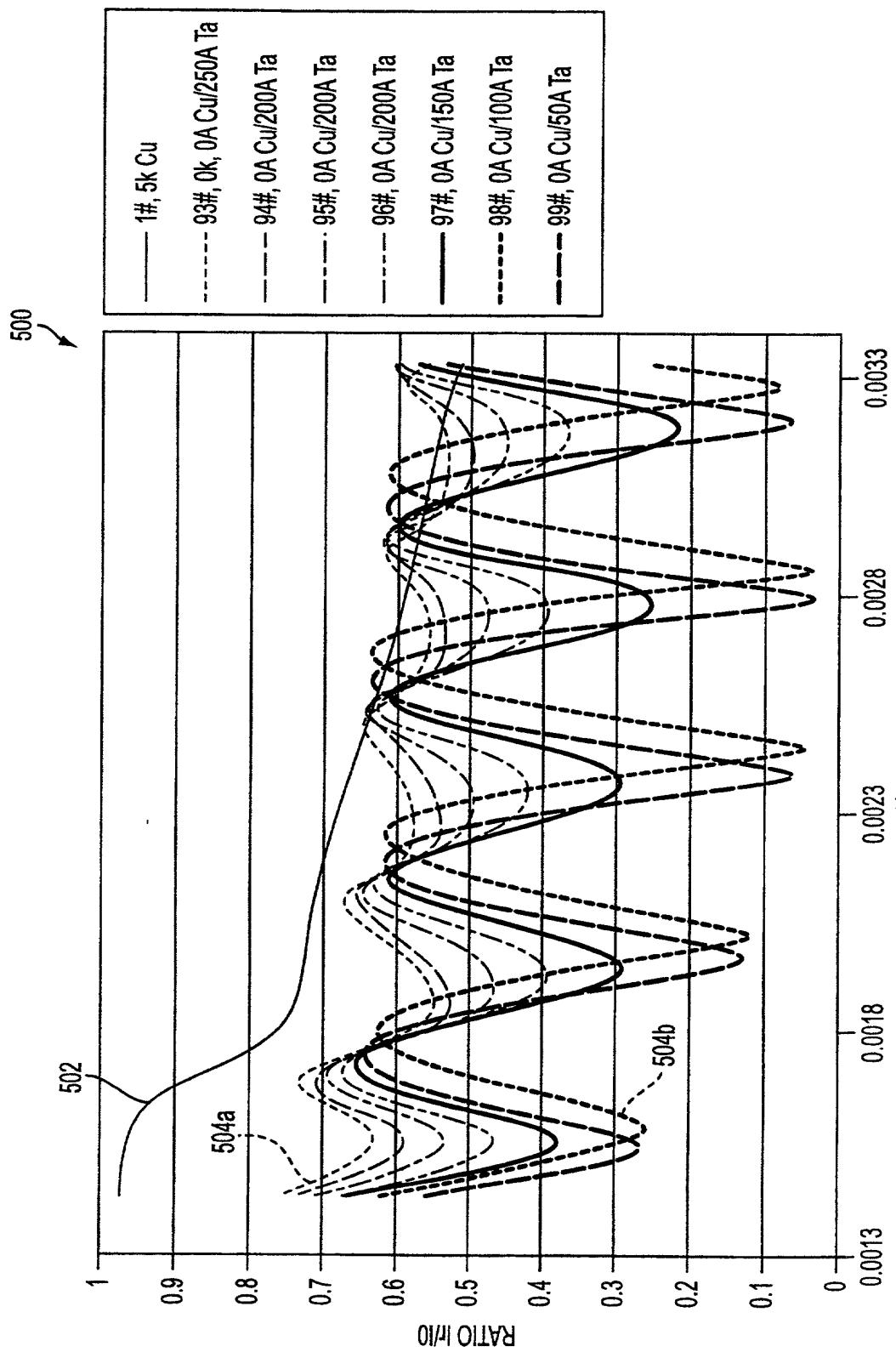
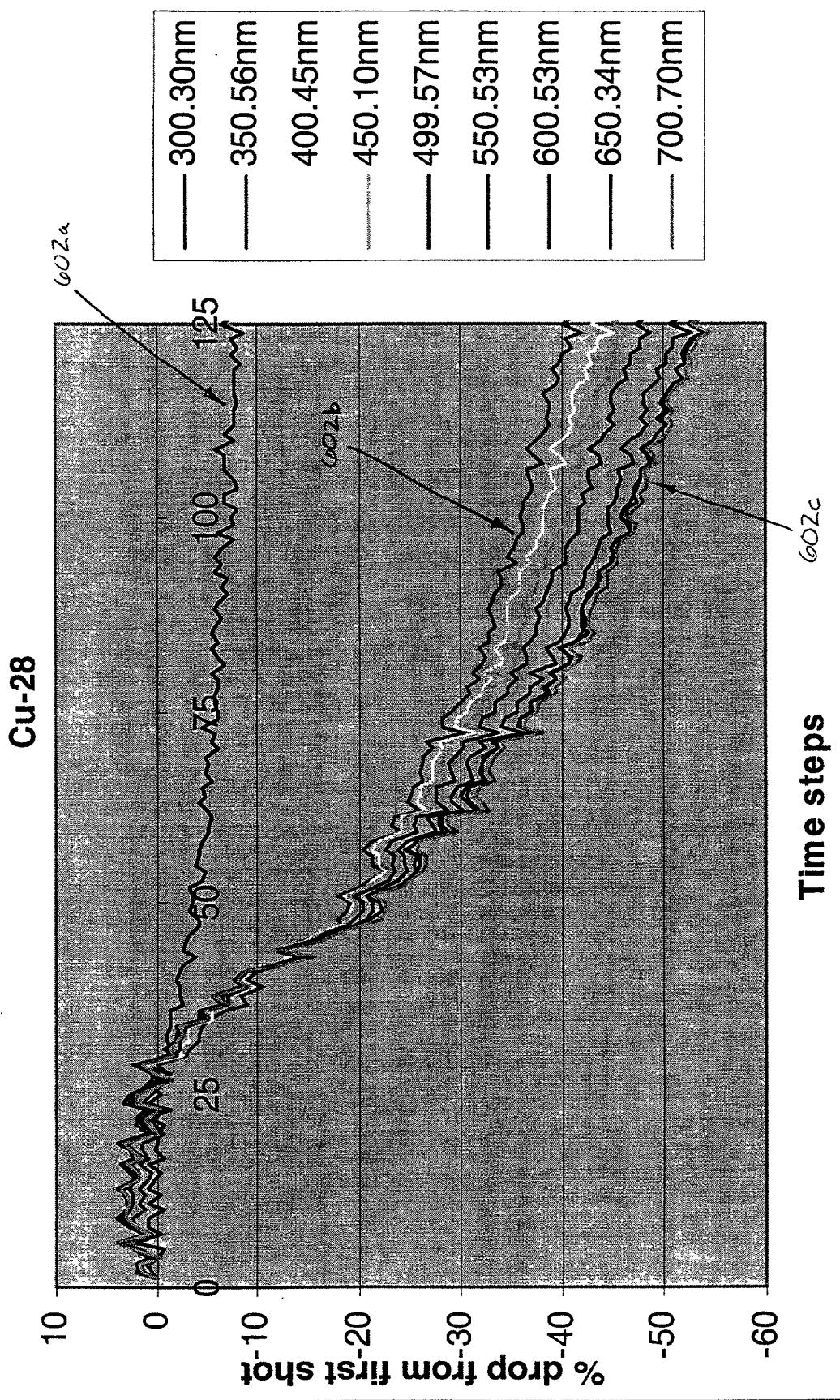
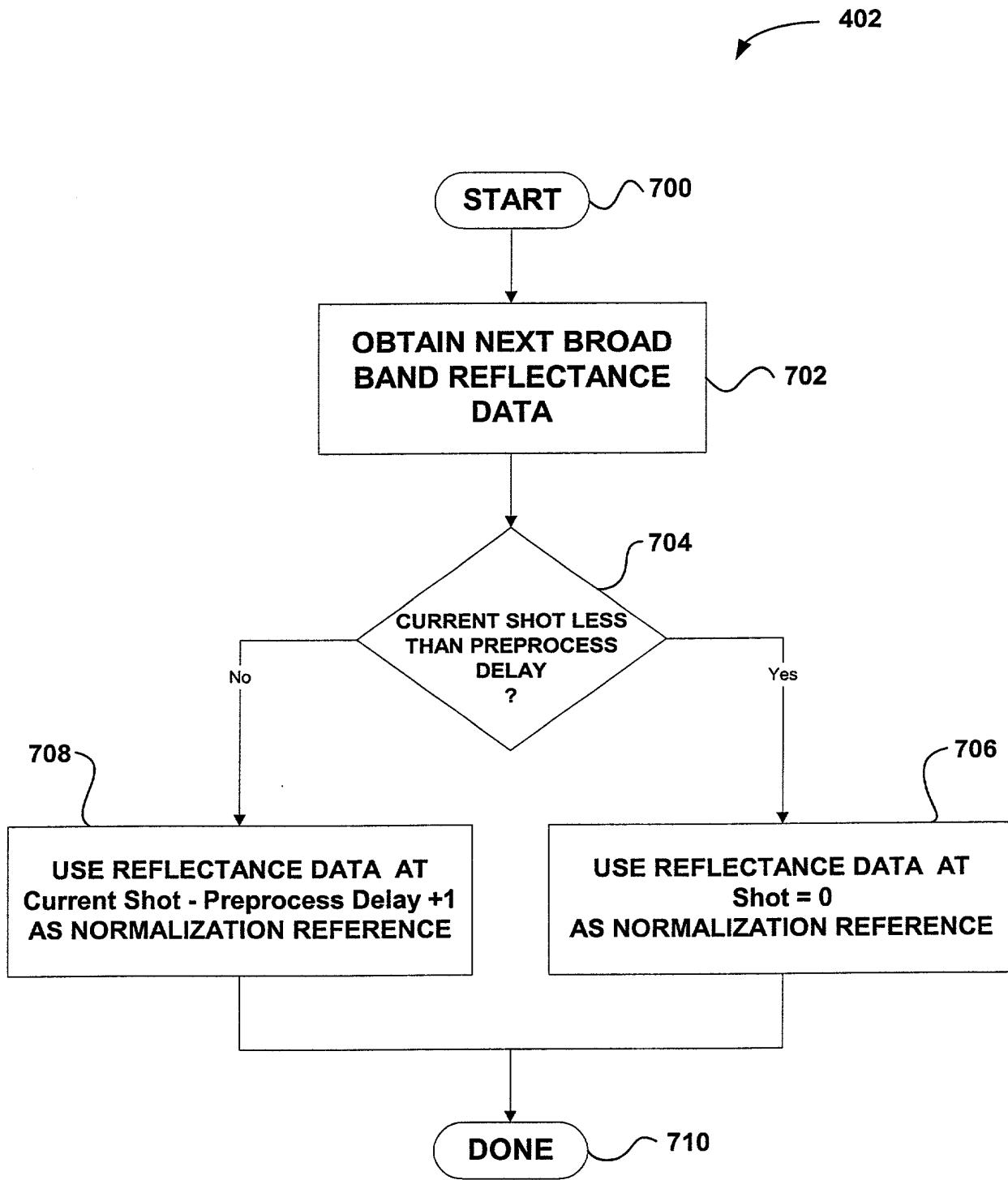


FIG. 5

FIG. 6





**FIG. 7**

Pre-Process Delay = Trailing Reference Delay

$\delta\sigma_{2a}$

$\delta\sigma_{2b}$

...

Trailing Reference Delay

Time step = Pre-Process Delay

Time Step = 0

Pre-Process Delay = Trailing Reference Delay

$\delta\sigma_{2a}$

$\delta\sigma_{2c}$

...

Trailing Reference Delay

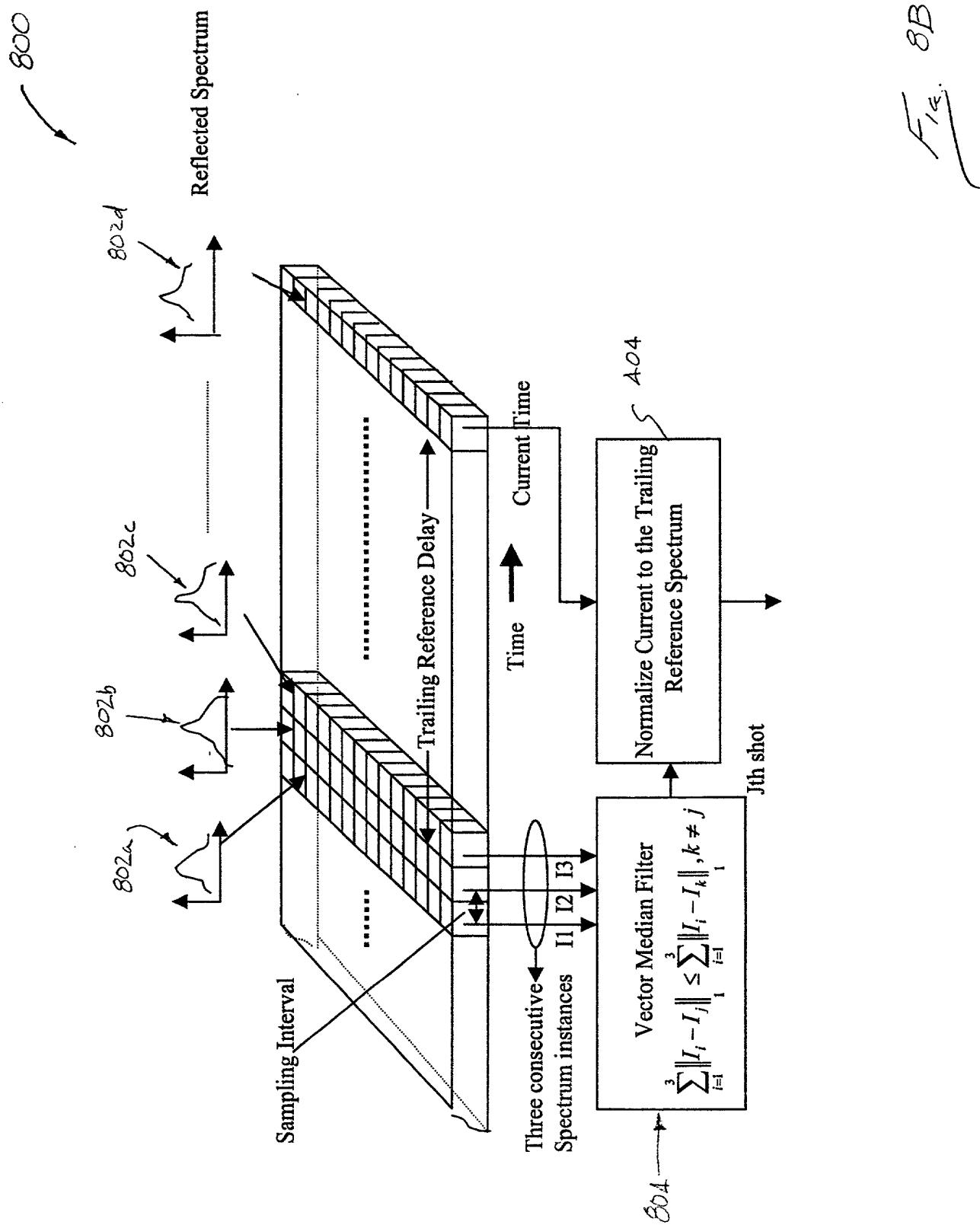
Time Steps → Current Time step

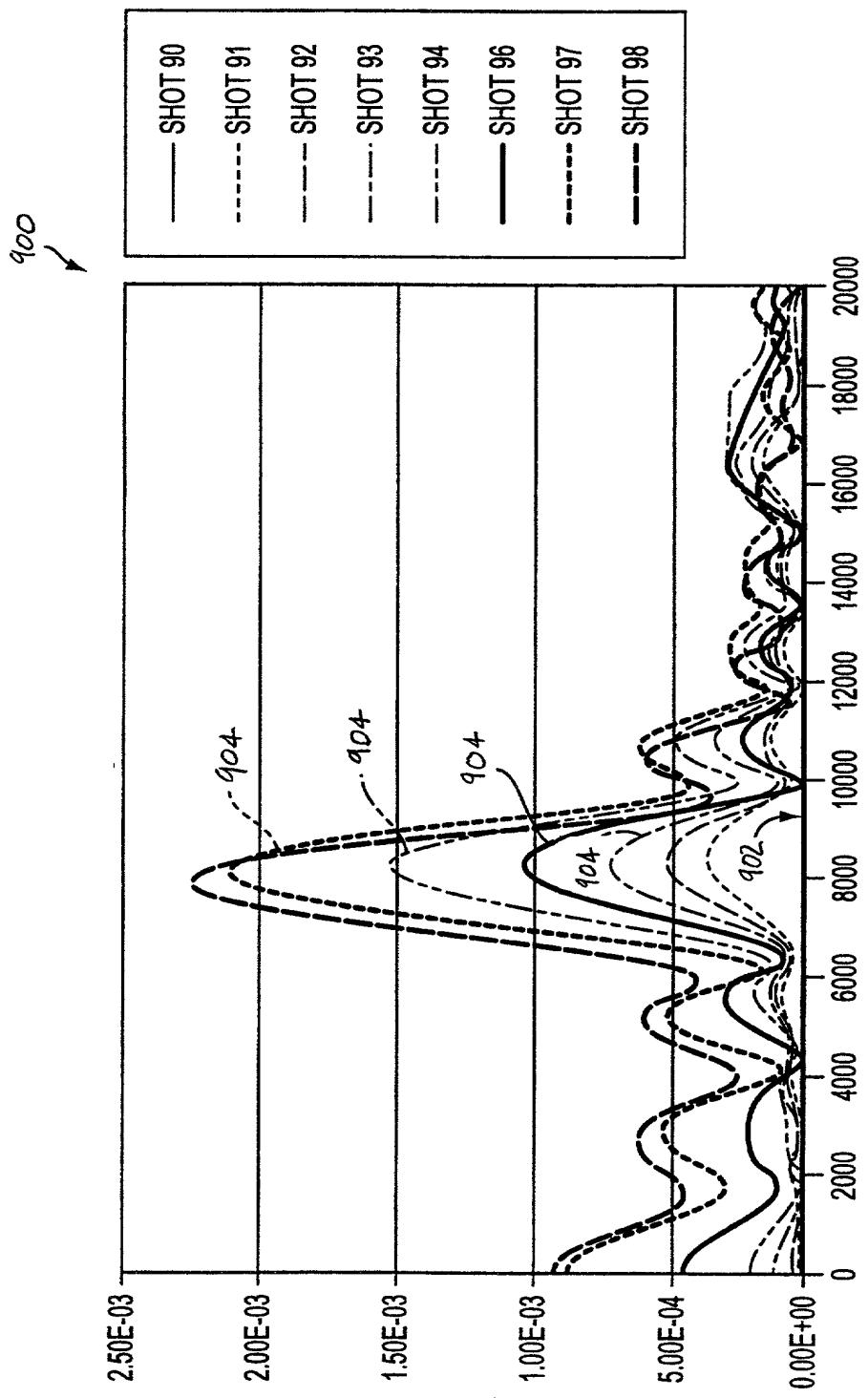
Normalize Current Spectrum  
using the Trailing  
Reference Spectrum

Rest of the algorithm

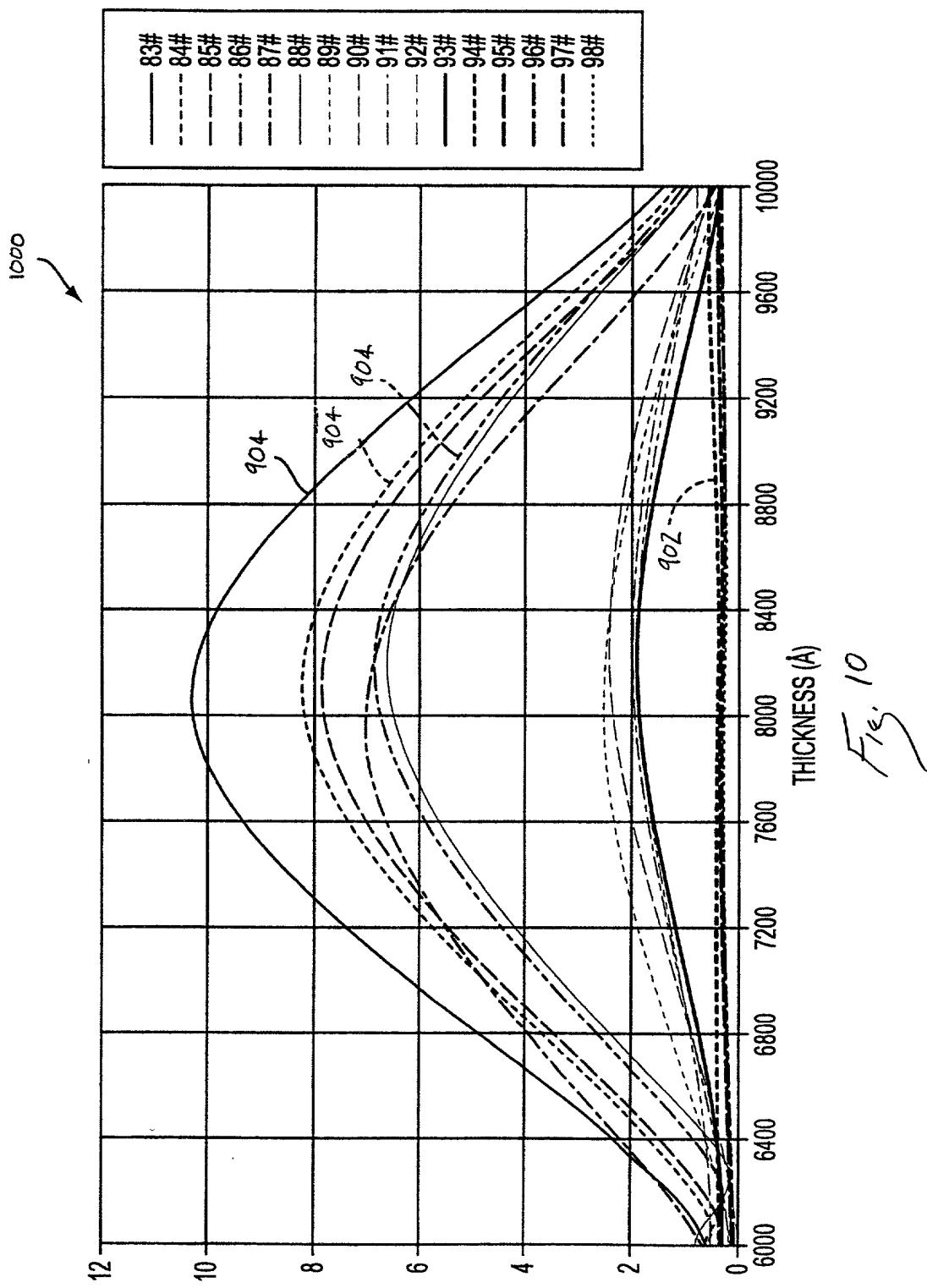
Fig. 8A

Figure 10.30: A block diagram of the processing of the reflected spectrum.





## THICKNESS IN A Fig 9



1100 1104 1102

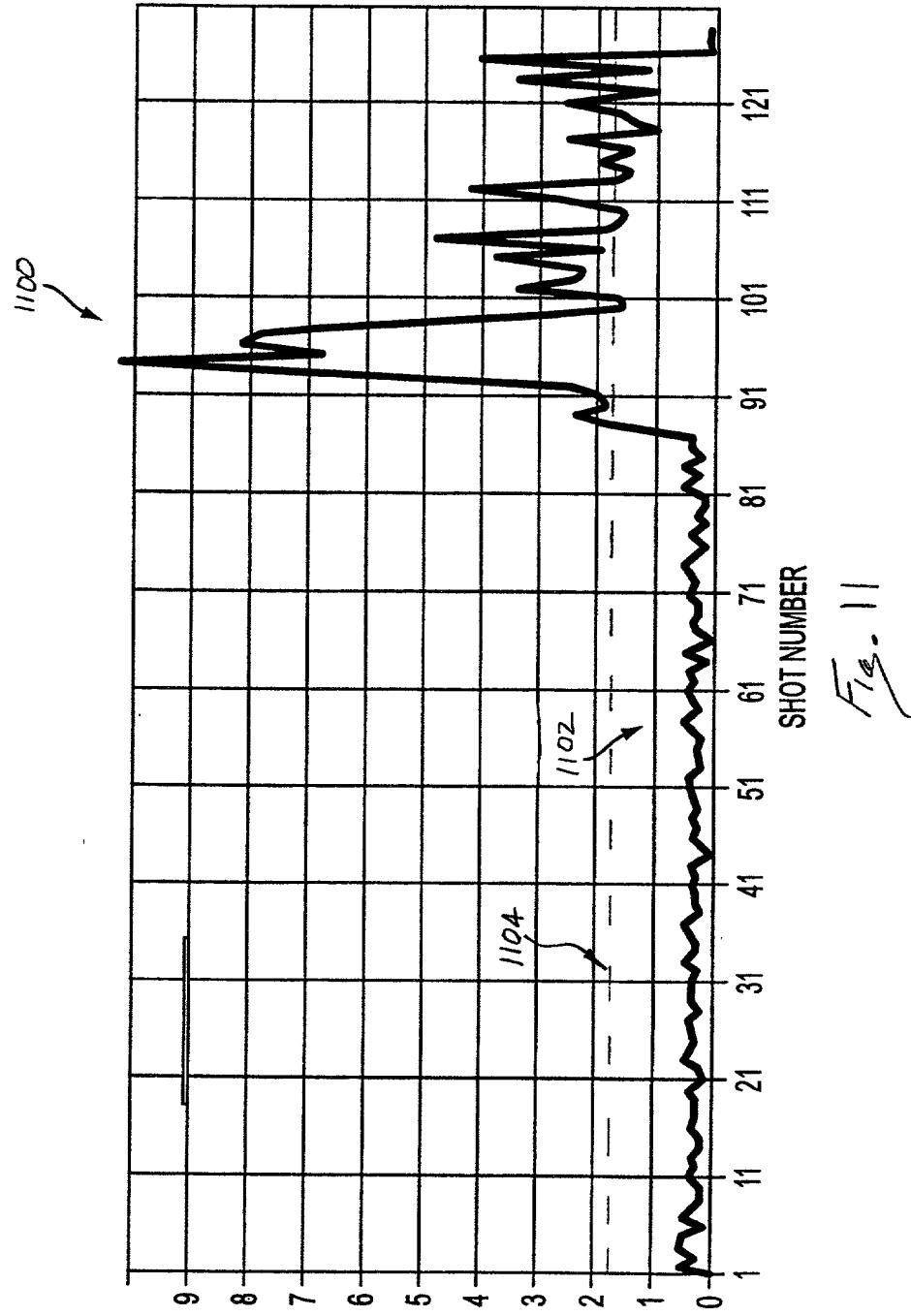
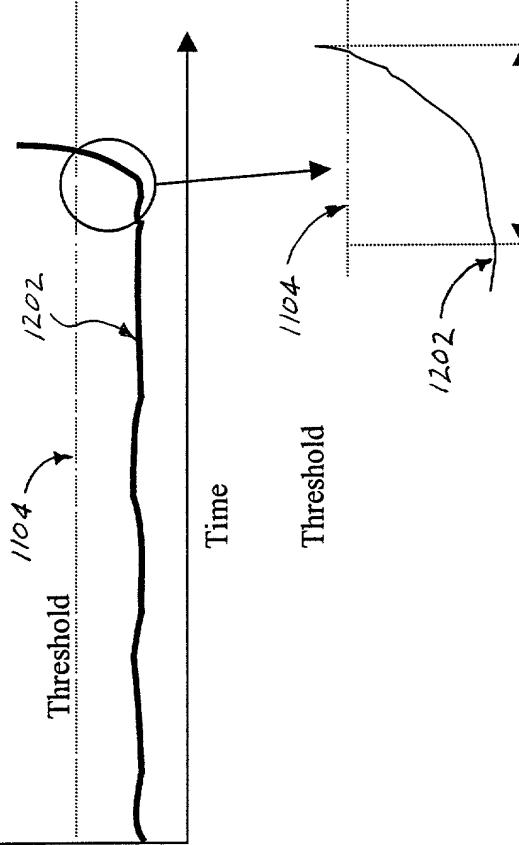


Fig. 11

1200

1000 900 800 700 600 500 400 300 200 100 0

Epd Signal



1202

Threshold

1104

1104

Threshold

1202

Trailing Reference Delay

1202

Exploded view

Fig. 12

**FIG. 13**

Time

